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By optical I/Os we mean the connections between the silicon photonic chip and optical wires connecting various photonic devices together. The cost of the optical connection to a silicon chip generally runs significantly higher than the cost of the silicon chip itself with challenges in optical bandwidth and port count scalability.

Silicon Nanophotonic Packaging - IBM

Silicon Nanophotonic Packaging - Photonic Flip-Chip. Concept. The core advantage of silicon photonics is large scale integration of devices on a single chip resulting in low-cost photonic circuits of high complexity. However, it is not always possible or practical to integrate all desired photonic materials on a silicon chip.

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Guiding readers all the way from basic calculations to optimizing fiber-chip coupling efficiency, this volume reflects the diversity of the field of packaging photonic devices and is the first and only comprehensive sourcebook on photonic assembly techniques.

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